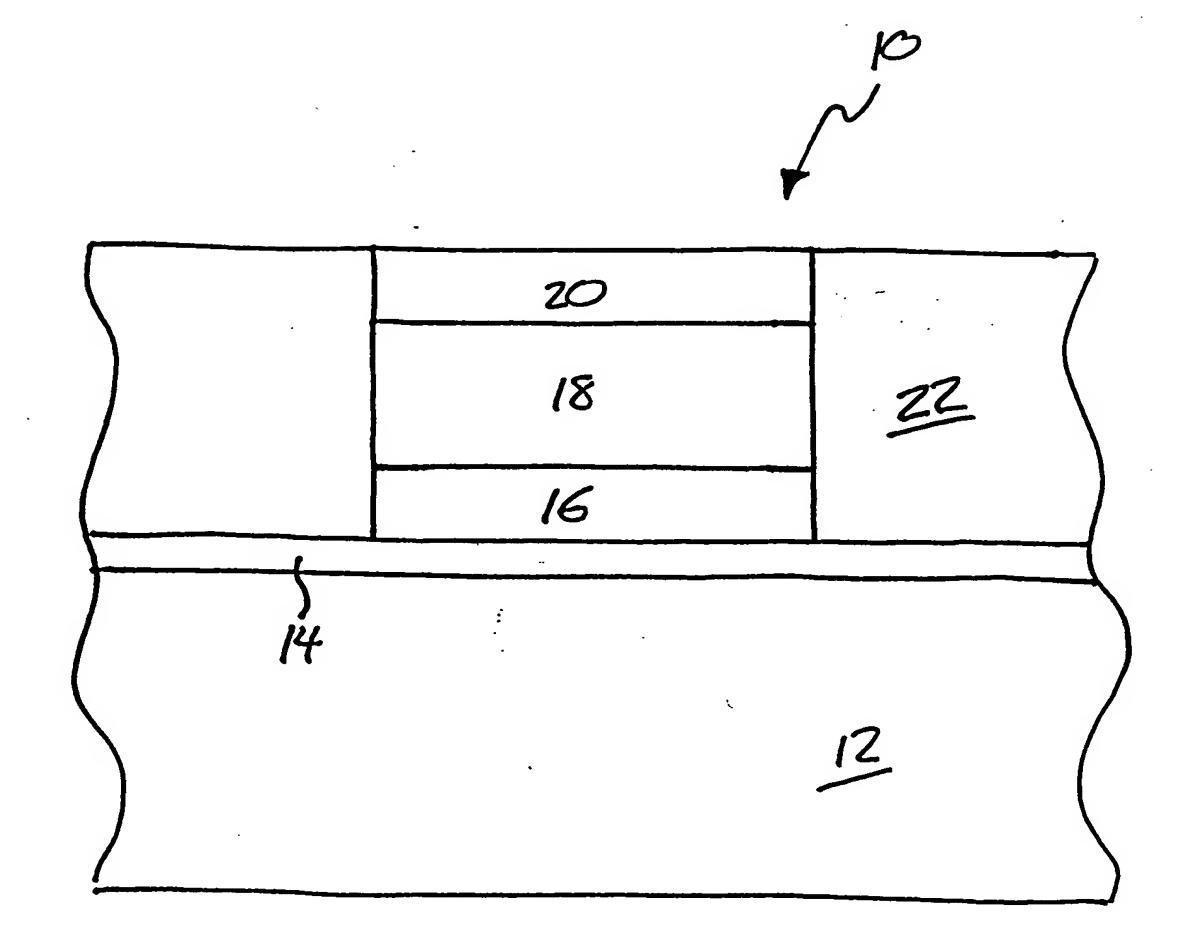
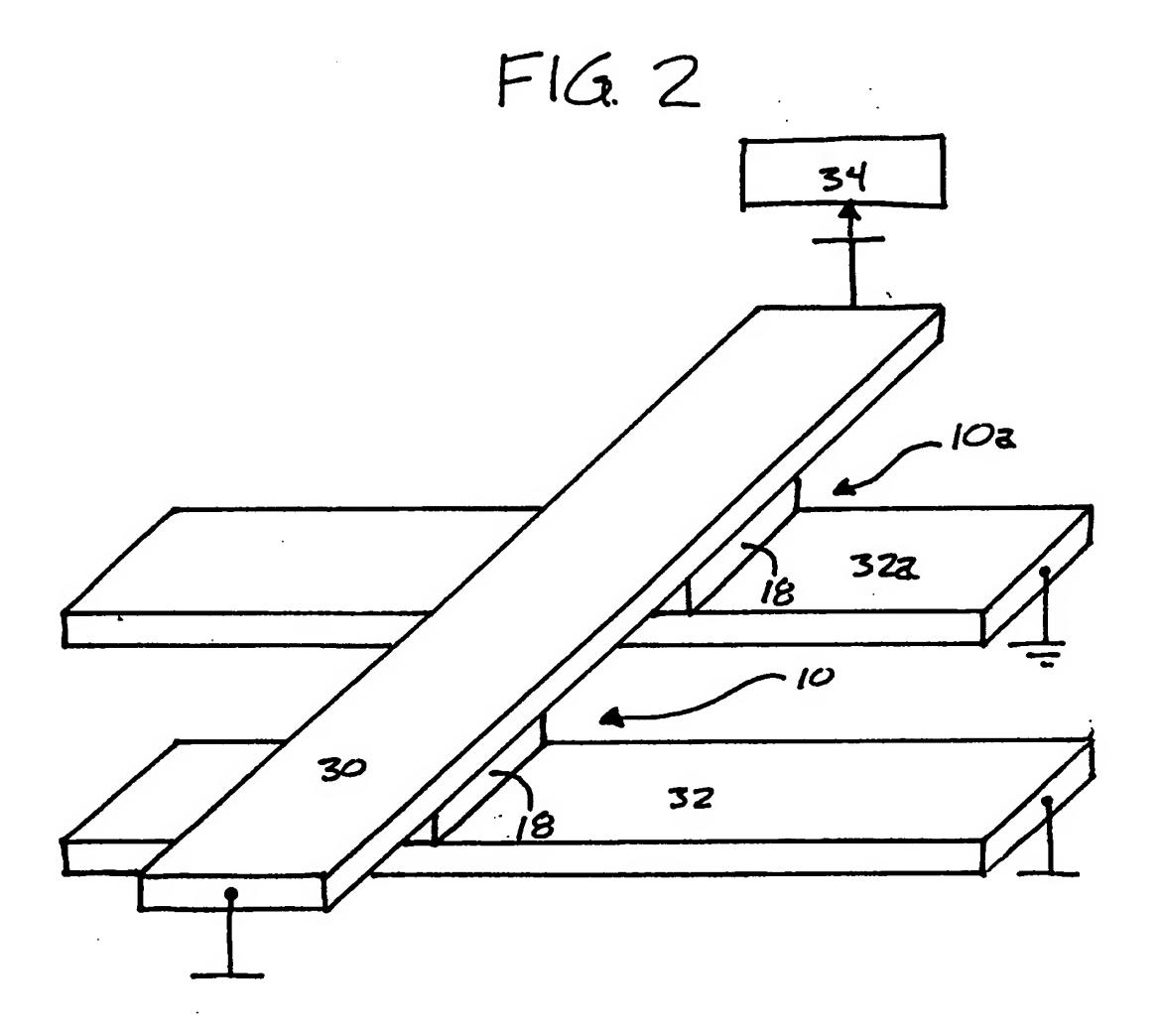
App No.: Not Yet Assigned Docket No.: M4065.1009 Inventor: Kristy A. Campbell et al.

Title: SIMULTANEOUS READ CIRCUIT FOR MULTIPLE Docket No.: M4065.1009/P1009

F16.1



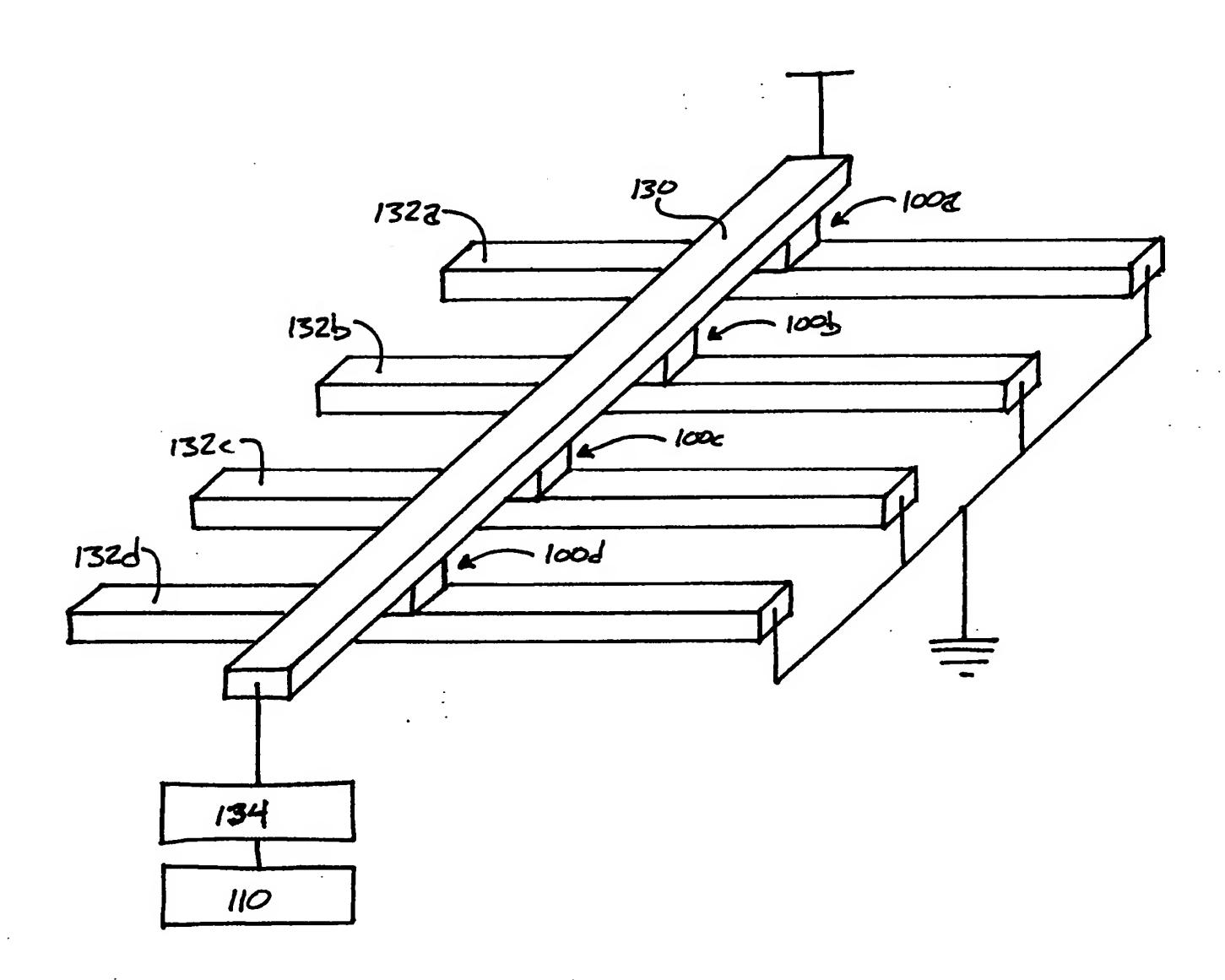
App No.: Not Yet Assigned Docket No.: M4065.1009
Inventor: Kristy A. Campbell et al.
Title: SIMULTANEOUS READ CIRCUIT FOR MULTIPLE
MEMORY CELLS Docket No.: M4065.1009/P1009



Docket No.: M4065.1009/P1009 App No.: Not Yet Assigned

Inventor: Kristy A. Campbell et al.
Title: SIMULTANEOUS READ CIRCUIT FOR MULTIPLE

FG.3

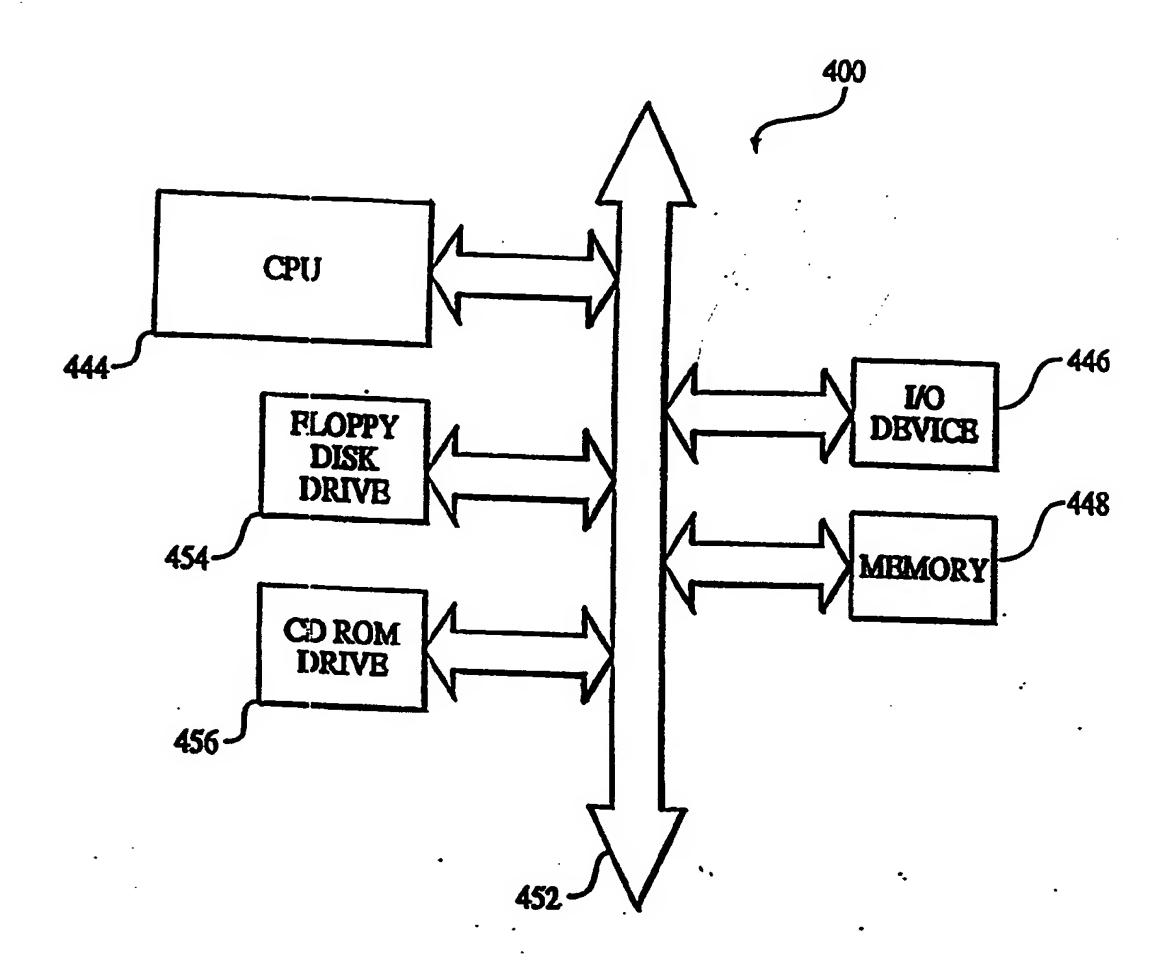


App No.: Not Yet Assigned Docket No.: M4065.1009/P1009

Inventor: Kristy A. Campbell et al.

Title: SIMULTANEOUS READ CIRCUIT FOR MULTIPLE

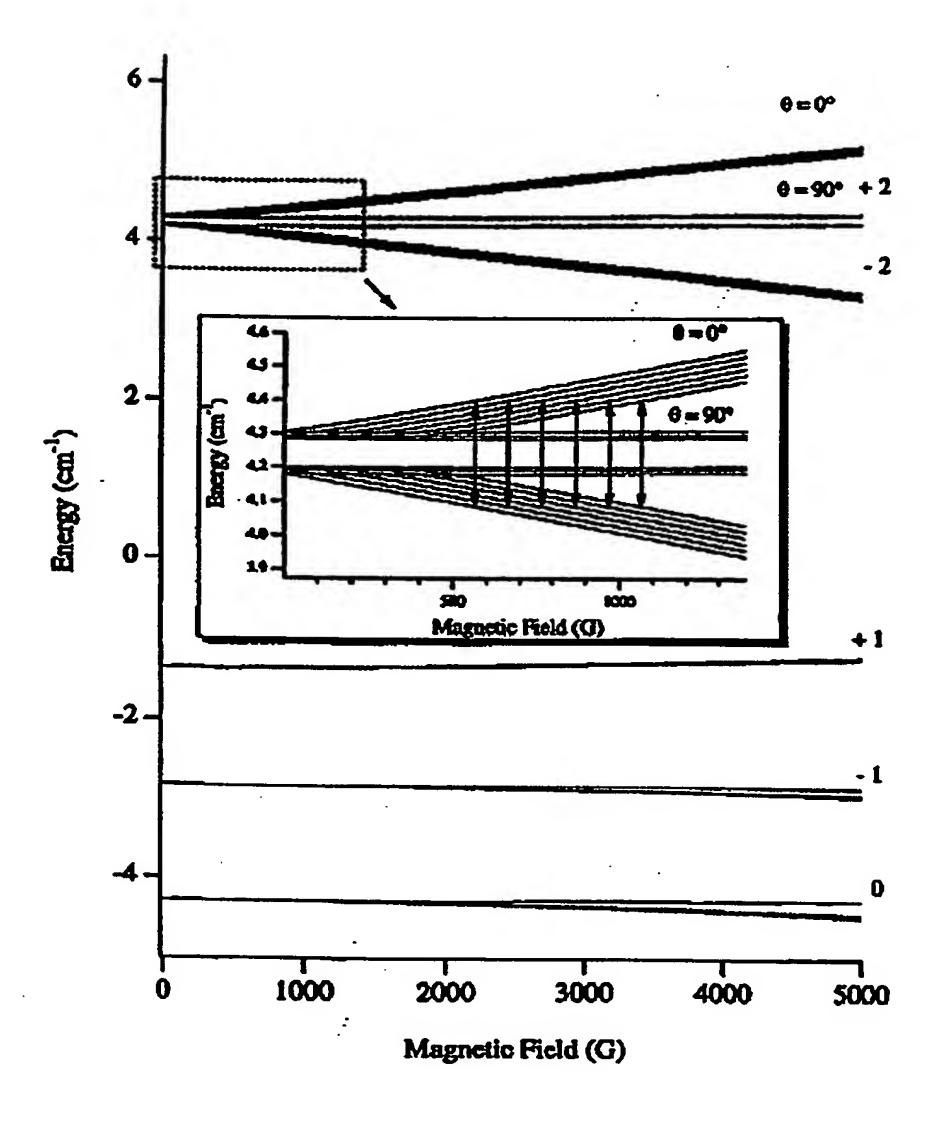
F16.4



App No.: Not Yet Assigned Docket No.: M4065.1009 Inventor: Kristy A. Campbell et al.

Title: SIMULTANEOUS READ CIRCUIT FOR MULTIPLE Docket No.: M4065.1009/P1009

FIG.5



App No.: Not Yet Assigned Docket No.: M4065.1009/P1009

Inventor: Kristy A. Campbell et al.

Title: SIMULTANEOUS READ CIRCUIT FOR MULTIPLE

FIG.6

